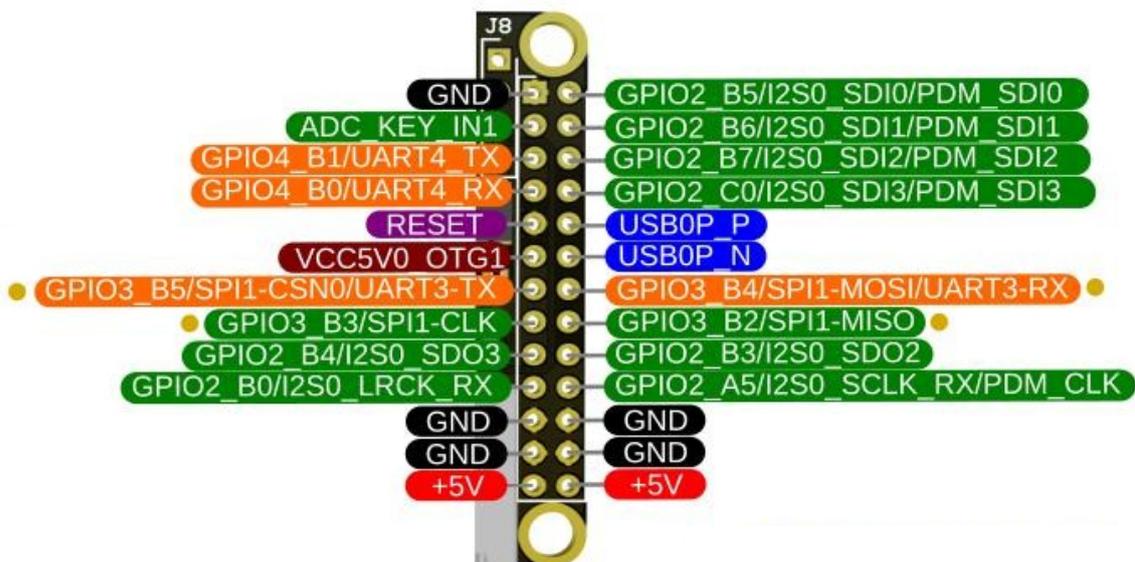
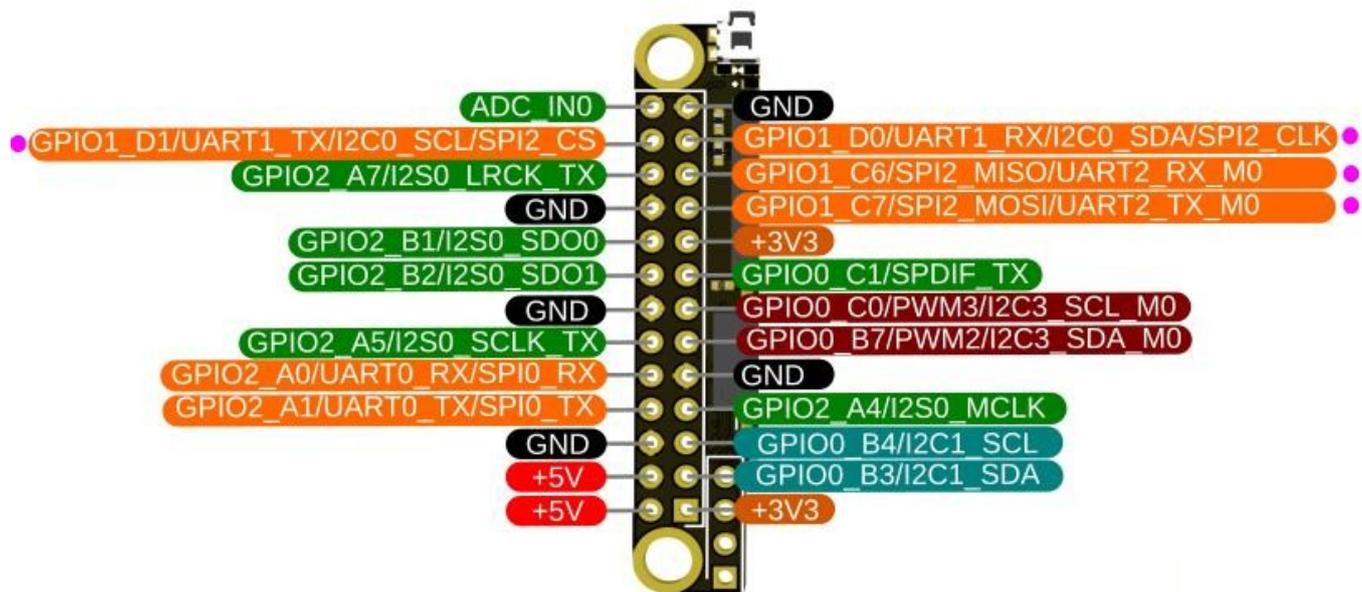


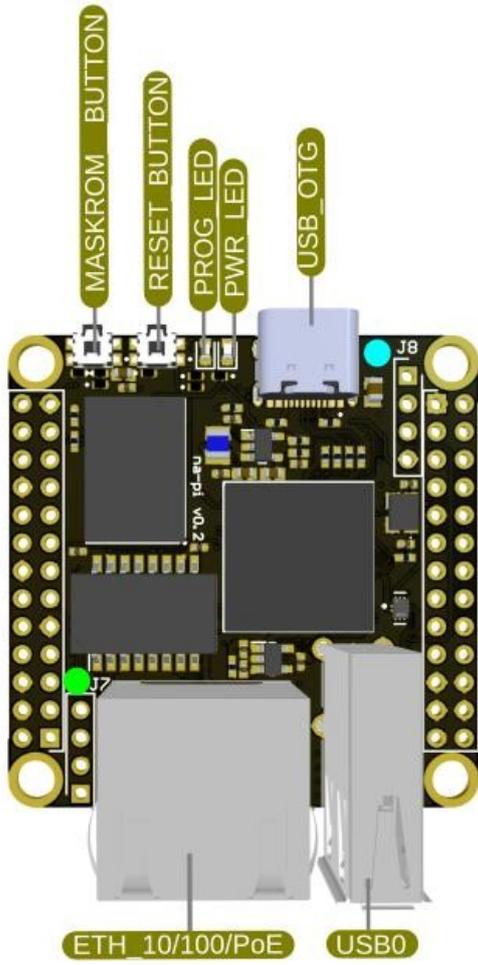
Схема GPIO NAPI C

Версия от 01.11.2023

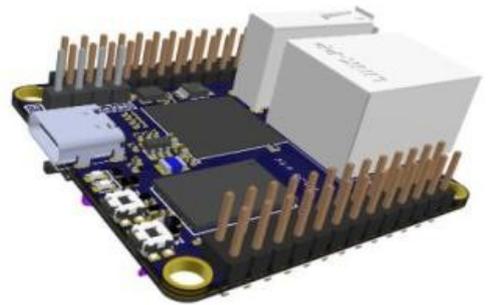


● SPI1 (вместо UART3 !)

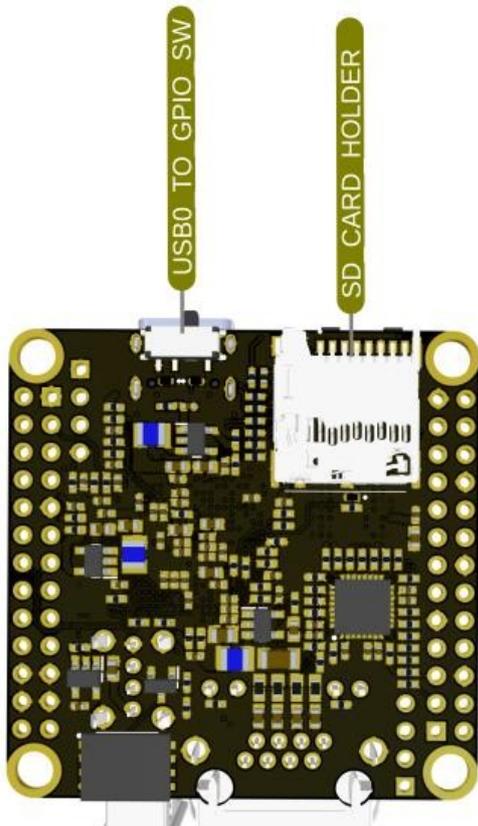
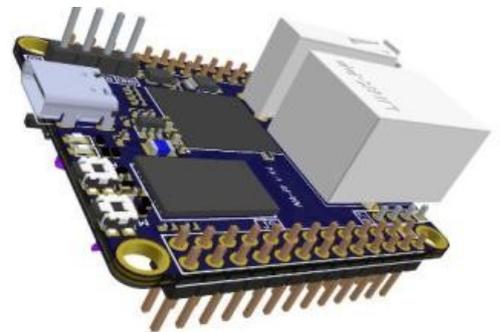
● SPI2 (вместо UART1\2 !)



PINS UP



PINS DOWN



№	Func1	Func2	Func3	Func4	Func5	Pin RK3308
HEADER J9						
1	+3V3 out					
2	+5V in					
3	GPIO0_B3	I2C1_SDA				V2
4	+5V in					
5	GPIO0_B4	I2C1_SCL				V1
6	GND					
7	GPIO2_A4					T15
8	GPIO2_A1	UART0_TX				V16
9	GND					
10	GPIO2_A0	UART0_RX				U14
11	GPIO0_B7		I2C3_SDA_M0			W1
12	GPIO2_A5					R15
13	GPIO0_C0		I2C3_SCL_M0			U2
14	GND					
15	GPIO0_C1					T1
16	GPIO2_B2					V15
17	+3V3 out					
18	GPIO2_B1					V14
19	GPIO1_C7		UART2_TX_M0	SPI2_MOSI		T9
20	GND					
21	GPIO1_C6		UART2_RX_M0	SPI2_MISO		W9
22	GPIO2_A7					V17
23	GPIO1_D0	UART1_RX	I2C0_SDA	SPI2_CLK		V10
24	GPIO1_D1	UART1_TX	I2C0_SCL	SPI2_CSN 0		T10
25	GND					
26	ADC_IN0					A19
HEADER J10						
1	GND					
2	GPIO2_B5					T14
3	ADC_IN1					B19
4	GPIO2_B6					U15
5	GPIO4_B1	UART4_TX				J16
6	GPIO2_B7					W14
7	GPIO4_B0	UART4_RX				F17
8	GPIO2_C0					Y14
9	RESET_in					
10	USB0_DP					B13
11	USB0_VCC5V0_out					
12	USB0_DM					A13
13	GPIO3_B5		I2C3_SCL_M1	SPI1_CSN 0	UART3_TX	T12
14	GPIO3_B4		I2C3_SDA_M1	SPI1_MOSI	UART3_RX	W12
15	GPIO3_B3			SPI1_CLK		R10
16	GPIO3_B2		SPI1_MISO			U13
17	GPIO2_B4					T17
18	GPIO2_B3					U16
19	GPIO2_B0					R16
20	GPIO2_A5					R15
21	GND					
22	GND					
23	GND					
24	GND					
25	+5V in					
26	+5V in					